

Sentaurus TCAD Tutorial

Giyeong Kim 2023.02.24

- Introduction to semiconductor detector
- Summary of TCAD tool
- Design of PiN sensor
- Silicon sensor fabrication
- Hands on session

Introduction to semiconductor detectors

- Semiconductor detectors have good energy resolution, so these detectors are widely used in high-energy collision experiments.
- When charged particles pass through the depletion region, they create electron-hole pairs.
- The created electron-hole pairs drifted in the electric field, thereby generating the signal.
- To increase the depletion region, reverse bias voltage is applied to the device.





Band gap





Density of state



 k_v

 k_x

Intrinsic semiconductor





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Intrinsic semiconductor

Intrinsic silicon detector에서의 S/N

실리콘에 입자가 지나갔을 때 생기는 signal과 background를 계산해보자.

Mean ionization energy I₀ = 3.62 eV, *dE/dx* = 3.87 MeV/cm, *n_i* ~ 1.45 · 10¹⁰ cm⁻³ at *T* = 300 K (Ge는 대략 1.5 · 10¹³ cm⁻³)

⊙ 두께 d = 300μm, 면적 A = 1cm²의 검출기에 MIP 입자가 지나간다면,

 $\frac{dE/dx \cdot d}{I_0} = \frac{3.87 \cdot 10^6 eV/cm \cdot 0.03 cm}{3.62 eV} \approx 3.2 \cdot 10^4 e^- h^+ pairs$

⊙ 같은 검출기 부피 내의 intrinsic charge carrier (T=300 K),

 $n_i dA = 1.45 \cdot 10^{10} cm^{-3} \cdot 0.03 cm \cdot 1 cm^2 \approx 4.35 \cdot 10^8 e^- h^+ pairs$

Thermal e^-h^+ 쌍의 수가 MIP 입자 signal 보다 만배정도나 더 많다!

⇒ 실리콘을 이용해 검출기를 만들려면 "depletion zone in reverse biased pn junctions"이 필요하다. Doping이 필요하다.

Extrinsic semiconductor



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The p-n junction

The p-n junction

overall region, space-charge neutral

$$\rho(x) = \begin{cases} 0 & x > x_n \\ eN_D & 0 < x < x_n \\ -eN_A & -x_p < x < 0 \\ 0 & x < -x_p \end{cases}$$

$$\frac{dV}{dx} = \begin{cases} -eN_D/\epsilon \cdot x + C_n & 0 < x < x_n \\ eN_A/\epsilon \cdot x + C_p & -x_p < x < 0 \end{cases}$$

$$= \begin{cases} -eN_D/\epsilon \cdot (x - x_n) & 0 < x < x_n \\ eN_A/\epsilon \cdot (x + x_p) & -x_p < x < 0 \end{cases}$$
as electric field E = -dV/dx must vanish for

$$x = x_n \text{ and } x = -x_p$$

$$W_{scr} = \left[\frac{2\epsilon(V_{bi} + V_0)}{e} \left(\frac{N_a + N_d}{N_a N_d}\right)\right]^{1/2} \qquad N_d < N_a \\ W_{scr} \approx 0.53\sqrt{\rho V_0}, (\mu m)$$

$$N_a < < N_d$$

 $W_{scr} \approx 0.32 \sqrt{\rho V_0}$, (μm)

The p-n junction

Figure 7.9 | Differential change in the space charge width with a differential change in reverse-biased voltage for a uniformly doped pn junction.

junction capacitance

$$C = \frac{\epsilon A}{W_{scr}}$$

Figure 7.12 | (a) Zener breakdown mechanism in a reverse-biased pn junction; (b) avalanche breakdown process in a reverse-biased pn junction.

Zener breakdown : high doped Avalanche breakdown : high electric field Signal

Poisson e.q

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Signal

	ε ₀ [@ 300 K]	ε ₀ [@ 77 K]	Egap
Si	3.6 eV	2.8 eV	1.1 eV
Ge	-	2.9 eV	0.7 eV

on average $E_x \sigma_x = E_i \sigma_i$	
$\sigma_i = \sqrt{N_i}, \sigma_x = \sqrt{N_z}$	- x

 ϵ_0 : mean energy per electron-hole pair

energy conservation

$$E_0 = E_i \cdot N_i + E_x \cdot N_x$$

 $E_i \cdot \Delta N_i + E_x \cdot \Delta N_x = 0$
 $E_0 = \text{const}$
 $\sigma_i = \frac{E_x}{E_i} \sqrt{\frac{E_0}{E_x} - \frac{E_i}{E_x}} N_i$, $N_i = \frac{E_0}{\epsilon_0}$
 $\sigma_i = \sqrt{N_i F}$, $F = \sqrt{\frac{E_x}{E_i} (\frac{\epsilon_0}{E_i} - 1)}$
: fano factor, 실리콘의 경우(300K) ~0.1

 E_i : ionization energy E_x : phonon excitation

TCAD(Technology Computer Aided Design) simulation

- TCAD simulators calculate the Poisson equation of the semiconductor properties in the numerical analysis method.
- TCAD simulators can visualize physical characteristics of semiconductors, such as electric field, potential, and current density.

Synopsys TCAD tool

- ε : electrical permittivity
- \vec{P} : ferroelectric polarization
- n(p) : electron(hole) density
- q : elementary charge

$\mu_{n(p)}$: electron(hole) carrier mobility

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- Electrostatic Potential $\nabla \cdot (\epsilon \nabla \phi + \vec{P}) = -q(p-n+N_{\rm D}-N_{\rm A}) - \rho_{\rm trap}$
- Carrier transport model $\nabla \cdot \overrightarrow{J_{n(p)}} = q(R_{net,n(p)} - G_{net,n(p))+}q\frac{\partial n(p)}{\partial t}$
- Drift-diffusion model $\overrightarrow{J_{n(p)}} = -n(p)\mu_{n(p)}\nabla\Phi_{n(p)}$ by Einstein relation

 $R_{net,n(p)}$: electron(hole) net recombination rate $G_{net,n(p)}$: electron(hole) net generation rate $\overrightarrow{J_{n(p)}}$: electron(hole) current density $\Phi_{n(p)}$: electron(hole) quasi_Fermi potential $N_{D}(N_{A})$: concentration of ionized donors(acceptors)

Synopsys TCAD tool

X Select DB Tool

Available All

SENTAURUS PROCESS SIMULATION, STRUCTURE GENERATION, IN

SENTAURUS GRID GENERATION

SENTAURUS DEVICE SIMULATION

SENTAURUS VISUALIZATION

Senstaurus tools

Sentaurus process

- An advanced 1D, 2D, and 3D process simulator
- Equipped with a set of advanced process models, which include default parameters calibrated with data from equipment vendors

Sentaurus structure editor

- Tools for creating geometric structures for TCAD simulation
- Direct CAD operations can be used to create 2D and 3D structures

Sentaurus device

- An advanced multidimensional (1D/2D/3D) devic e simulator
- Characterize electrical, thermal and optical behavi or of semiconductor devices

Synopsys TCAD tool

X Select DB Tool

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SWB (Sentaurus WorkBench)

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SDE & SDEVICE

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Simulation setup

Leakage current result with the final design

Process flow co- ETRI

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PR PR PR PR PR	nt pt pt pt nt	10000Å 8000Å ~15000Å ↓ ↓ ↓ ↓ ↓ 1000Å	م n+ p+ p+ p+ n+	n ⁺ p ⁺ p ⁺ n ⁺
N-type silicon N+	N-type silicon	N-type silicon N ⁺	N-type silicon N ⁺	N-type silicon N ⁺
16-1. Active Patterning 16-2. P ⁺ implant [Active] B11, 50KeV, 1.0E15/cm2	17. PR strip	18-1. TEOS 8,000A 18-2. Anneal (950°C, 30min)	19. #CONT Patterning	20. Oxide etch (Dry+Wet) Target: ~10000A (oxide on n+)
n* p+ p+ p+ n* N-type silicon	n ⁺ p ⁺ p ⁺ p ⁺ n ⁺ N-type silicon	PR n+ p+ p+ p+ n+ N-type silicon	n* p+ p+ n+ N-type silicon	n ⁺ p ⁺ p ⁺ p ⁺ n ⁺ N-type silicon
N*	N+	N ⁺	N*	N*
21. PR strip	22. metal deposit [10000Å]	23. PR coating	24. #GRD metal Patterning	25. metal etching
	n ⁺ p ⁺ p ⁺ n ⁺	nt pt pt nt	n+ p+ p+ n+	nt p ⁺ p ⁺ p ⁺ nt

26. PR strip

27. metal deposit[400Å] barrier metal split

28. PR coating

29. #ACT metal Pattering

30. metal etching

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Photomask designed

Photomask layout of 8" wafer using AutoCAD tool

Align key for NNFC

Performance of fabricated chip

Hands on session

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 GFTPApplicationNotes
 GettingStarted
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->Save (경로 STDB)

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Tools -> Add -> SDE

Boundary File 클릭 -> SDE 창이 뜨면 file -> Journal -> on 클릭(기록용)

Draw -> exact coordinate -> 사각형 클릭, 단위는 um, silicon, aluminum등 선택

Contacts -> contact Sets -> electrode 만들기

-> select body에서 select edge로 바꾸고 none부분을 지정하고자 하는 이름으로 변경

taurus Structure Editor@localhost.localdomain P-2019.03 (1.1114)	-	
Yew Draw Mesh Device Contacts Help Select Edge Select Edge		
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Particle Profile Placement		
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ler:rebuild)		
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e o:set-contact-edges (list (car (find-edge-id (position 5 2.5 0)))) "cathode")	10 · 44	
ler:rebuild)		
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	Change Placement Delete Placement Close	

Doping concentration 정하기

Electrode 라인 클릭 후 Set contact 클릭

X Sentaurus Structure Editor@localhost.localdomain P-2019.03 (1.1114)	– 🗆 X
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<u>₩</u> lild Mesh	Aluminum p_region anode
	cathode
	Define Ref/Eval Window
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	X2 10.000000 Y2 10.000000 Z2 Modify
	Refinement Definition
	Name RefinementDefinition_all
	X Direction Y Direction Z Direction
	Max Element Size 0.5 0.5
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0 (sdegeo:set-contact-edges (list (car (find-edge-id (position -1 5 0)))) "anode")	
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0 >(sdegeo:set-current-contact-set "cathode")	
"anode" (sdegeo:set-contact-edges (list (car (find-edge-id (position 11 5 0)))) "cathode")	Create Refinement Delete Refinement Close
- <(render:rebuild)	
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#t <pre>// state: stat</pre>	

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Mesh 설정

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1 C) લે 🔒 🛛 ၈ ભ	Define Rel/Eval Window	▼ none ▼ Select Body ▼			D New Ctrl+N :on
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성된 구조 저장하기 sh -> Build Mesh

-> Journal -> off

sdevice

